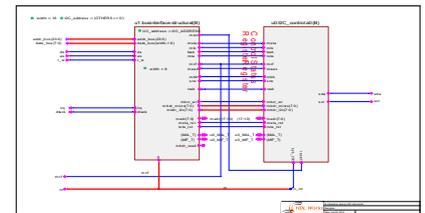


Company overview

HDL Works continued in 2004 as a management buyout of Transfer/Translogic. We are both an EDA Vendor and distributor. Our customers range from small design houses to multinationals like ASML, BAE Systems, National Instruments, Siemens and Thales.

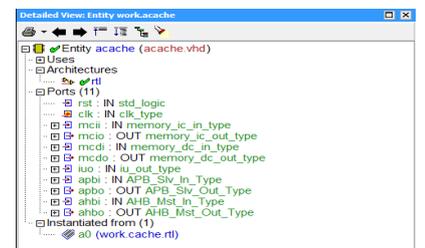
EASE: Graphical HDL Design Entry

- Block diagram, State diagram, Truth table and Text editor
- SystemVerilog and VHDL
- Excellent language support with records and generate statements.
- Automated design consistency
- HTML & SVG documentation



HDLCompanion: HDL Analysis and navigation environment

- The HDL designer's Swiss army knife.
- Provides a good overview of any HDL Design
- Integrated SystemVerilog and VHDL syntax checkers
- HDL Linter
- Name convention checking



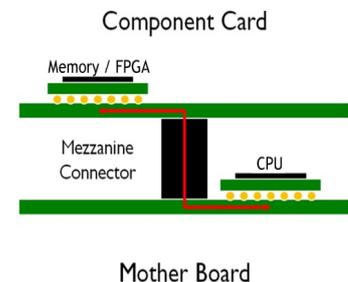
IO Checker: Verify FPGA connections between PCB and FPGA environment

- Intelligent matching using regular expression
- Power & Ground verification
- LVDS pair verification
- Constraint file generation
- Various sorting and filtering options
- Schematic capture symbol connectivity

Pin	Bank	IO type (Pin file)	Signal (Pin file)	Signal (PCB)
T1	34	<2.50>		PWR_1.8 (1.8V)
M3	34	<2.50>		PWR_1.8 (1.8V)
F8	0			GROUND
R4	34	<2.50>		PWR_1.8 (1.8V)
T7	14	LVC MOS18 (12, SLOW)	vidBlue	vidGreen
T8	14	LVC MOS18 (12, SLOW)	vidGreen	vidBlue
H10	0		<DONE_0>	DONE
K7	0		<DXN_0>	GROUND
K3	0		<DXP_0>	GROUND
K10	0		<INIT B_0>	INIT

ConnTrace: Visualize/verify connections between components on one or multiple (connected) PCBs

- Uses schematic pin labels in visualization
- Intelligent matching using regular expression
- Power & Ground verification
- LVDS pair verification
- Visualize FPGAs as a virtual boards
- OpenVPX support
- Module verification



HDL Works also offers custom software development, tool-flow scripting and consultancy. Our main expertise is in the area of lexical analysers and parsers, HDL Languages and EDA tools. Examples of previous custom software development are a BOM part lister, Fujitsu FLDL netlist and FTDL test format translator into VHDL.

More information can be found on our website.

You can contact us at info@hdlworks.com or by telephone on: +31 (0)318 642022